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**AMENDMENTS TO THE CLAIMS:**

This listing of the claims will replace all prior versions, and listings, of the claims in this application.

Claims 1-18 have been cancelled.

**Listing of Claims:**

1. (Cancelled).
2. (Cancelled).
3. (Cancelled).
4. (Cancelled).
5. (Cancelled).
6. (Cancelled).
7. (Cancelled).
8. (Cancelled).
9. (Cancelled).
10. (Cancelled).
11. (Cancelled).
12. (Cancelled).
13. (Cancelled).
14. (Cancelled).
15. (Cancelled).

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16. (Cancelled).

17. (Cancelled).

18. (Cancelled).

19. (Previously Presented) In a system comprising a host coupled to a storage subsystem, a method for operating a write-through cache with a two phase commit technique for logical configuration cache update, comprising:

during phase one, and in response to a submission of a configuration change transaction, constructing a request; and

preparing change data for the cache and storing the prepared change data as a change pending for the cache;

sending the request for processing;

upon receipt of the completed request, checking a return code to determine success or failure of the request; and

if the request completed successfully, the change data is applied to the cache for updating the cache with the contents of the storage subsystem, thereby executing phase two of the two-phase commit procedure; else

if the request failed, the change data is deleted without being applied to the cache.

20. (Previously Presented) In a system comprising a host coupled to a storage subsystem, the host comprising a write-through cache, said host further comprising a programmed data processor for operating the write-through cache with a two phase commit technique for logical configuration cache update, said data processor operating during a first phase, in response to a submission of a configuration change transaction, for constructing a request and for preparing and storing change data for the cache as a change pending for the cache; said data processor sending the request for processing and, responsive to a receipt of a completed request, checking a return code to determine success or failure of the request; said data processor being responsive to a condition

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where the request completed successfully for applying the change data to the cache for updating the cache with the contents of the storage subsystem, thereby executing phase two of the two-phase commit procedure; else said data processor is responsive to said request failing for deleting the change data without applying the change data to the to the cache.

21. (Previously Presented) The host of claim 20, where said host is coupled to said storage subsystem through an adapter.

22. (New) An apparatus for updating of cache data in a storage system, the apparatus comprising:

a memory for storing data;

a cache for storing data associated with the memory;

at least one processor for preparing change data for updating the cache, the at least one processor comprising circuitry for

during phase one, and in response to a submission of a configuration change transaction, constructing a request for change to the memory; and

preparing change data for the cache and storing the prepared change data as a change pending for the cache;

sending the request for processing;

upon receipt of a signal from the memory representative of completion of the request for change, checking a return code to determine success or failure of the request; and

if the request completed successfully, the change data is applied to the cache for updating the cache with the contents of the storage subsystem, thereby executing phase two of the two-phase commit procedure; else

if the request failed, the change data is deleted without being applied to the cache.

23. (New) The apparatus of claim 22, wherein the storage system comprises a disk storage subsystem.

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24. (New) The apparatus of claim 23, wherein the memory is comprised in a disk adapter.
25. (New) The apparatus of claim 23, wherein the memory is comprised in a disk controller.
26. (New) A computer program storage device readable by a machine and comprising executable computer program instructions for updating of a cache in a storage system, the storage system comprising a memory holding data and a cache holding data associated with the memory, the instructions for performing the method of:

during phase one, and in response to a submission of a configuration change transaction, constructing a request; and

preparing change data for the cache and storing the prepared change data as a change pending for the cache;

sending the request for processing;

upon receipt of the completed request, checking a return code to determine success or failure of the request; and

if the request completed successfully, the change data is applied to the cache for updating the cache with the contents of the storage subsystem, thereby executing phase two of the two-phase commit procedure; else

if the request failed, the change data is deleted without being applied to the cache.